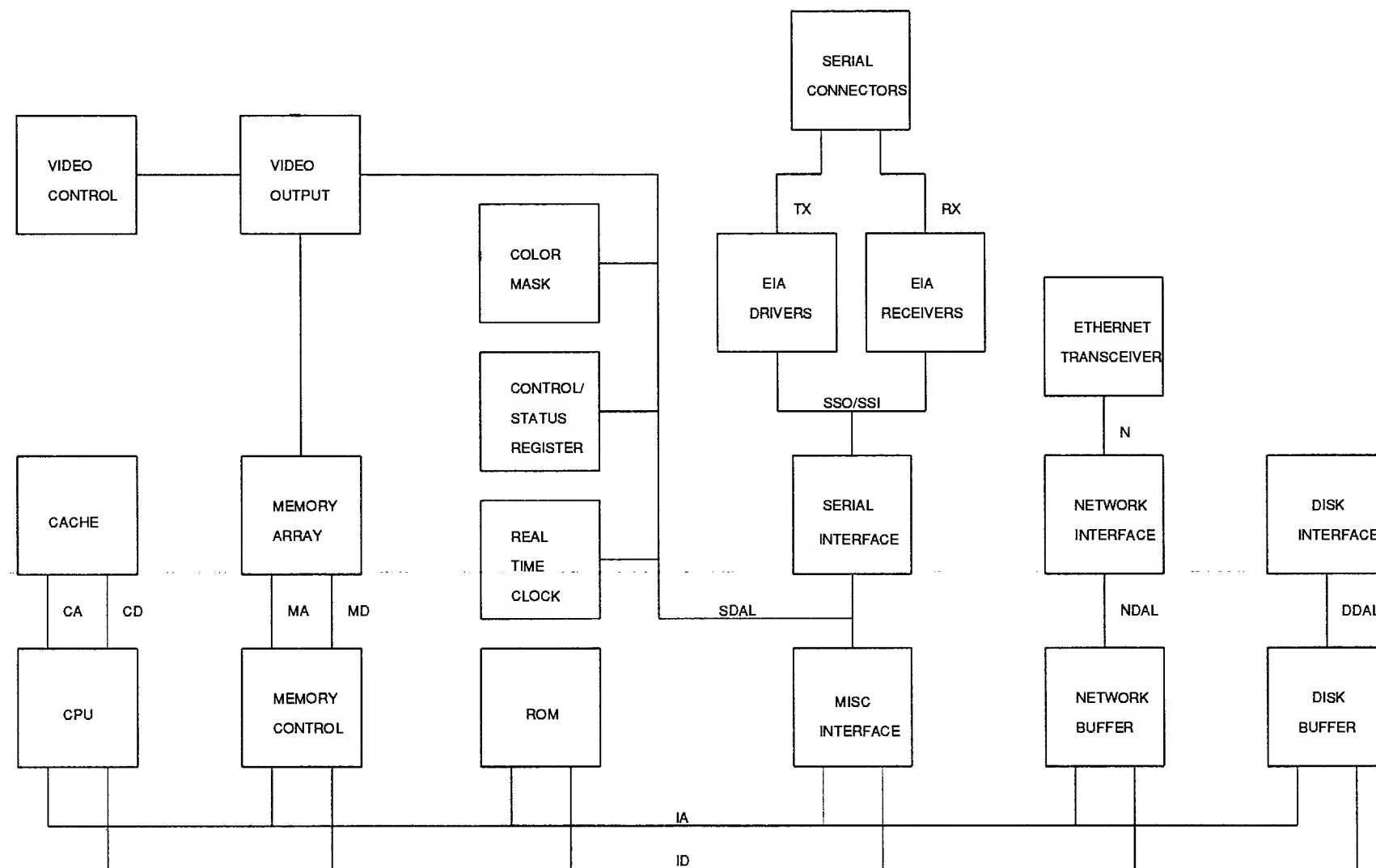


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REVISION		
CHK	CHANGE NO.	REV.

digital

USRA:
FIRST USED ON OPTION MODEL:

DRN.

CHK'D.

DATE

DATE

ENG.

M. NIELSEN

BOARD LOCATION:

SHEET

TOP DOCUMENT NUMBER:

DATE
31 OCT 88

1 OF 21

TITLE:

PMAX System Module

SIZE

B

CODE

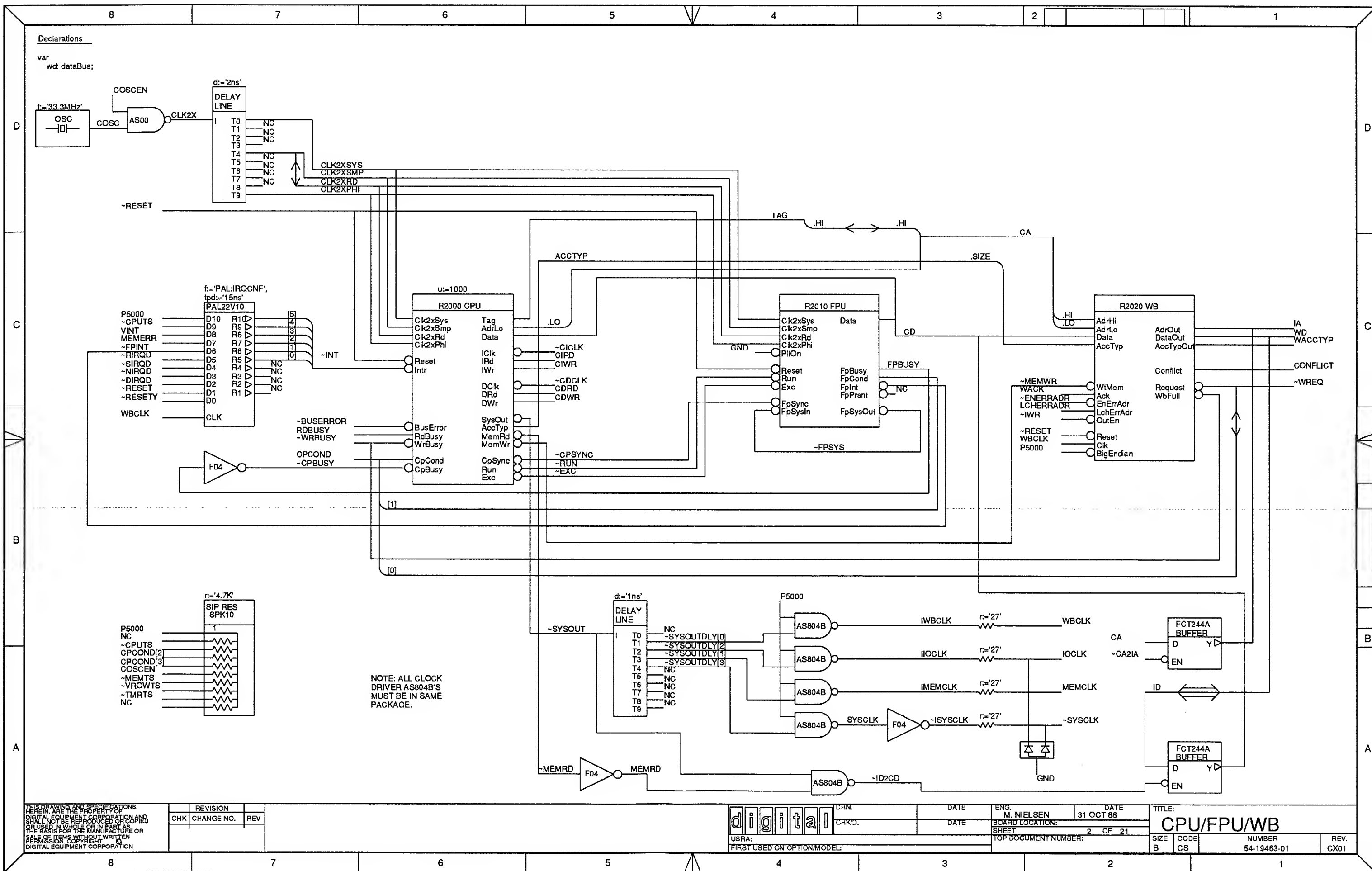
CS

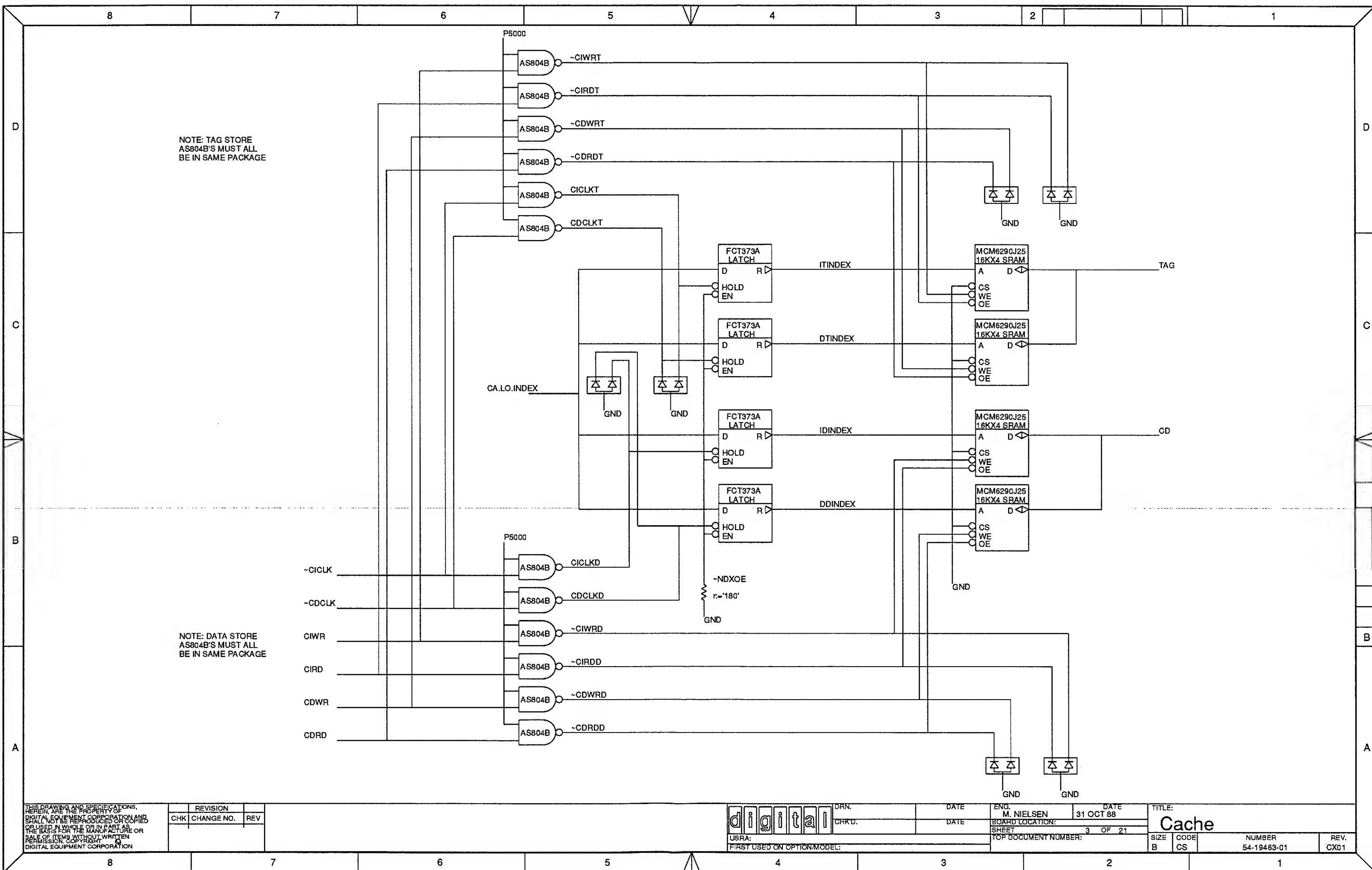
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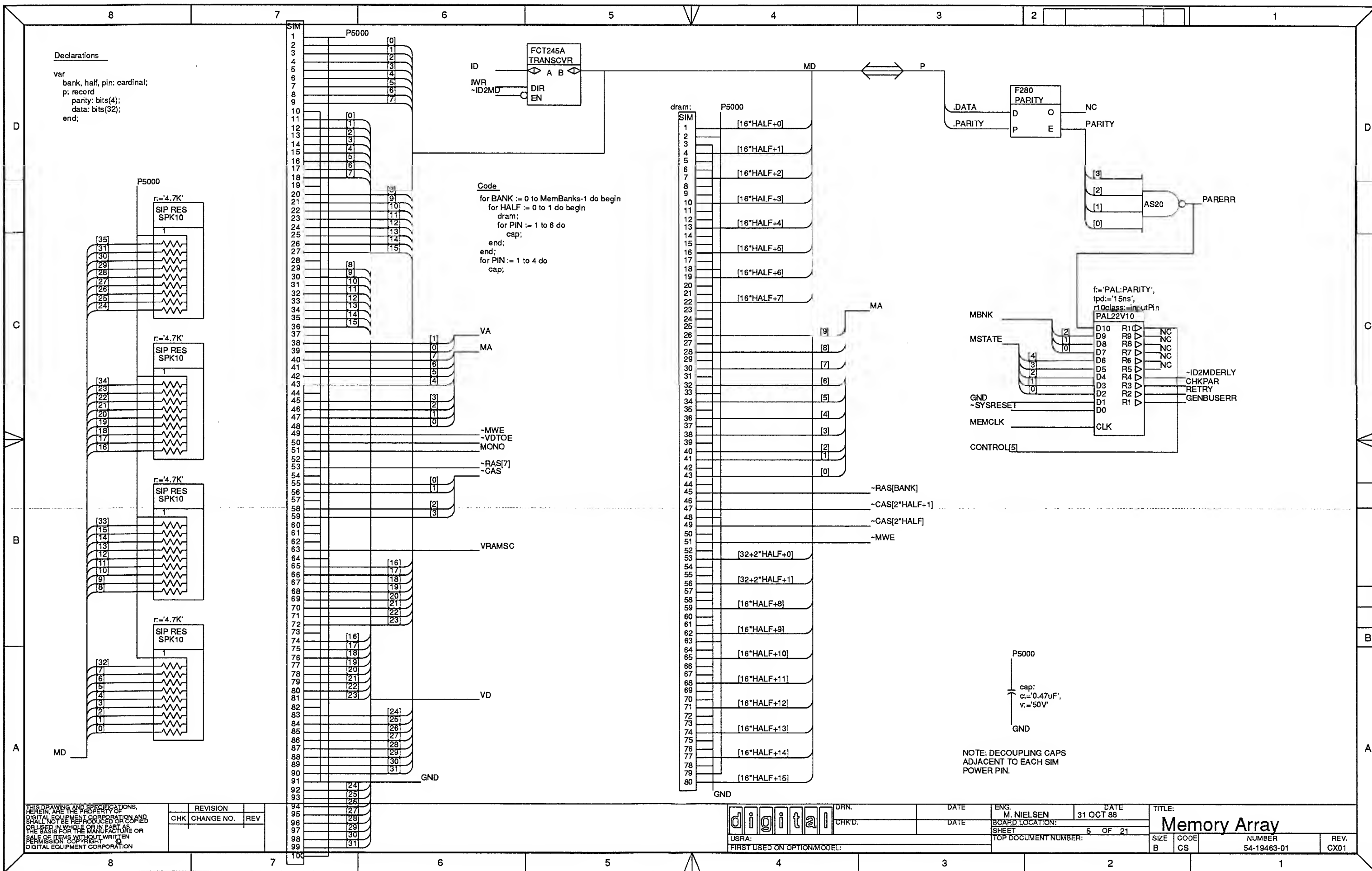
54-19463-01

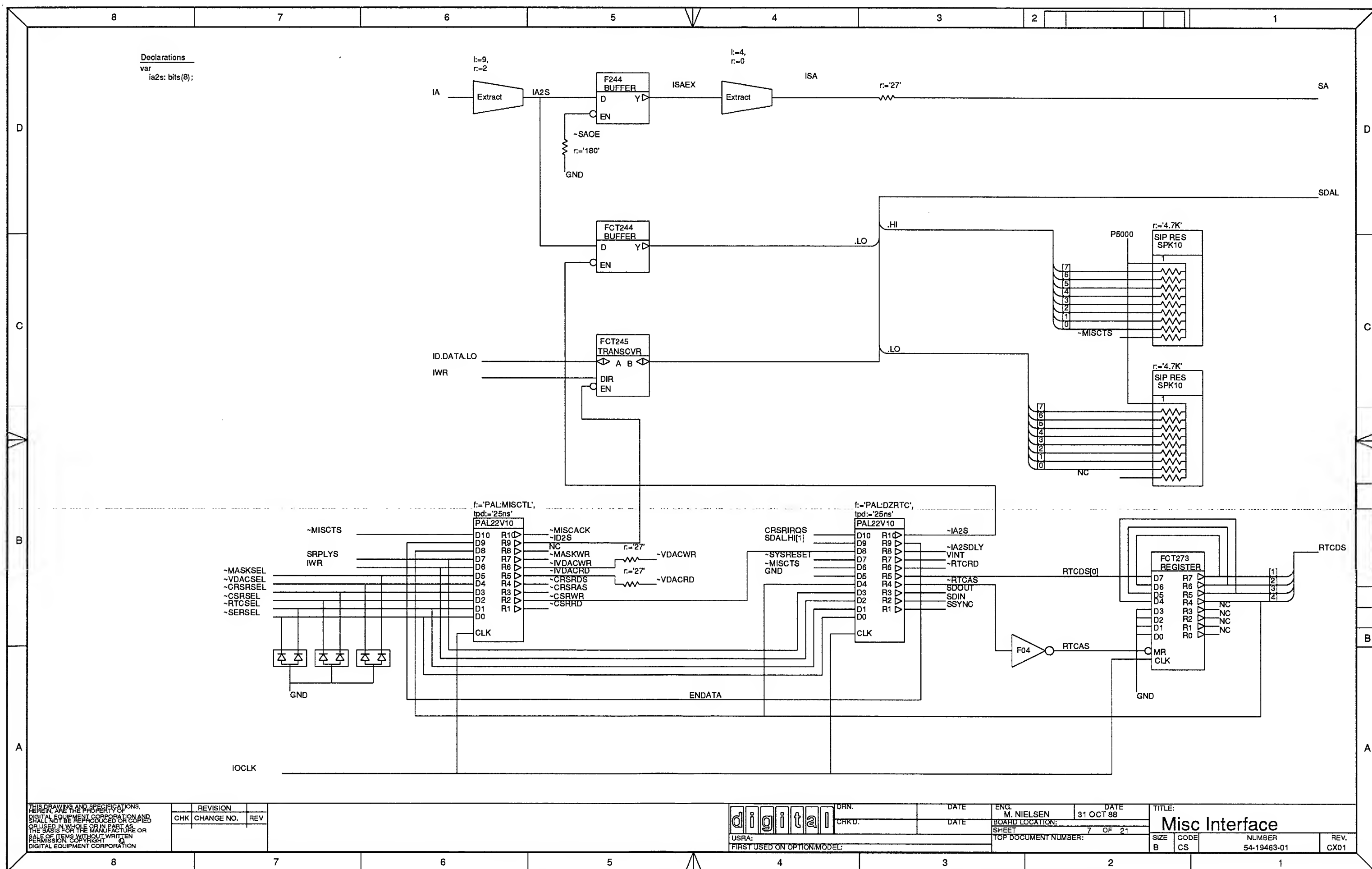
REV.

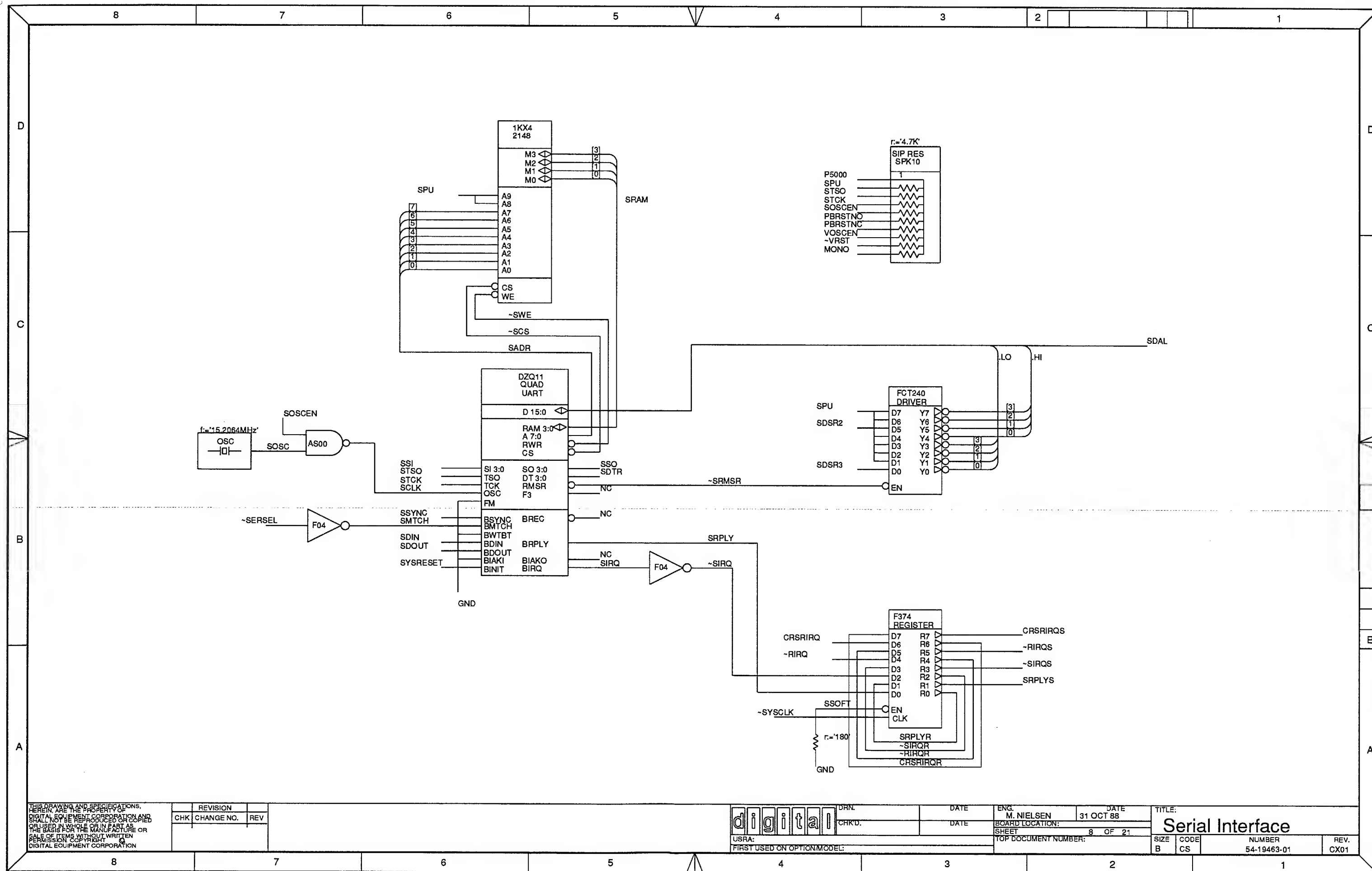
CX01

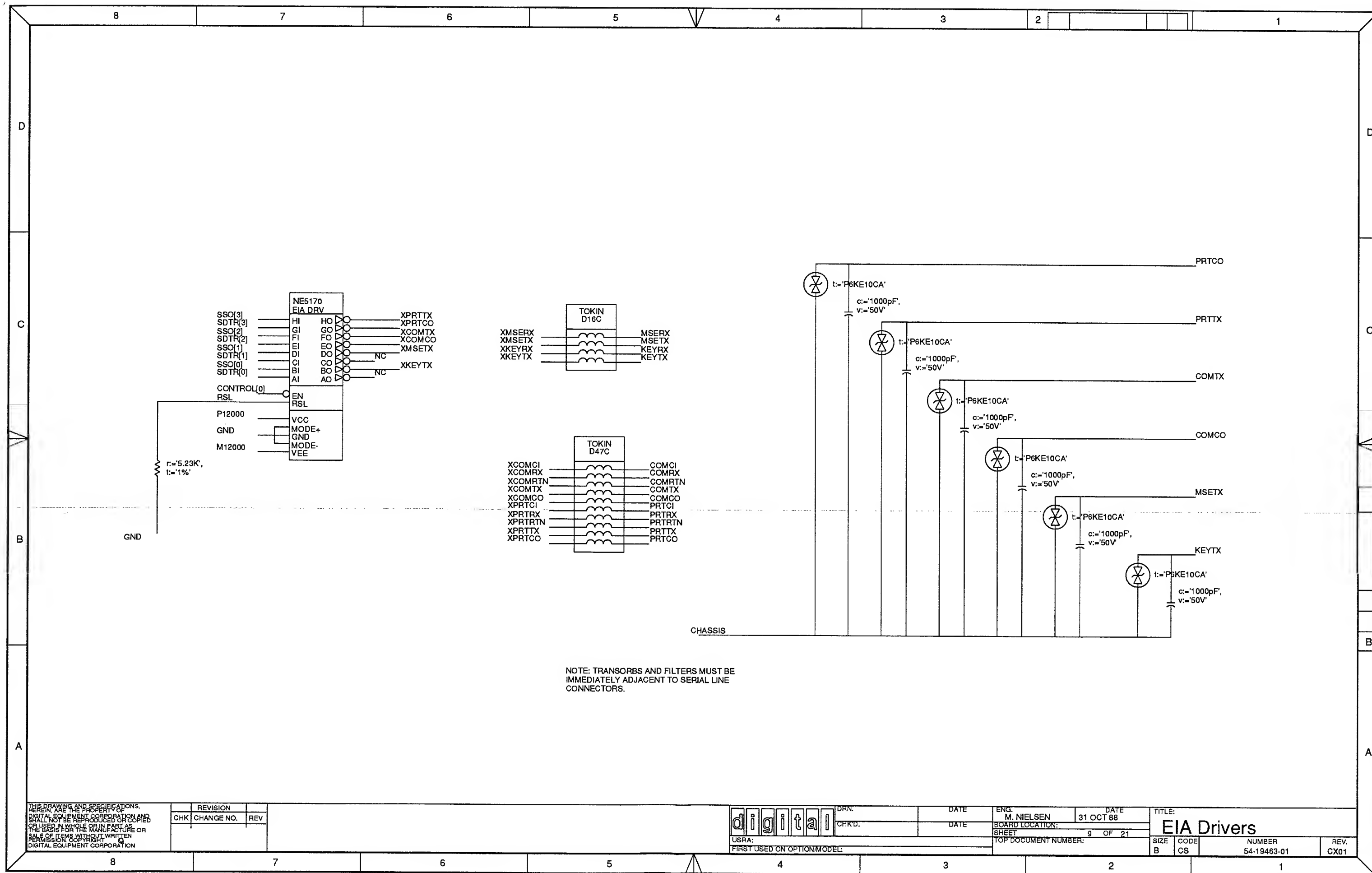












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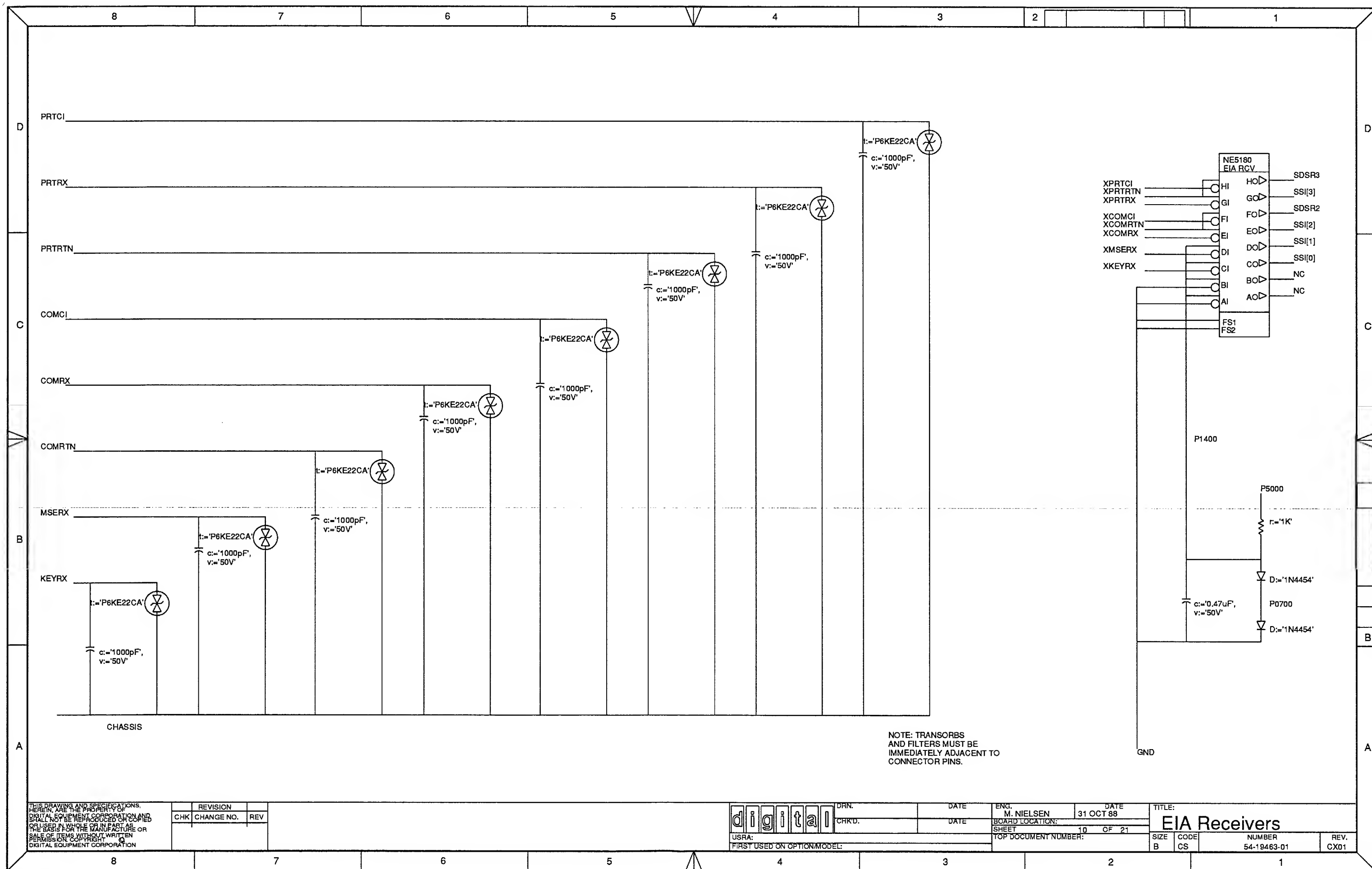
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CHK'D:

DATE
DATE

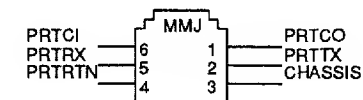
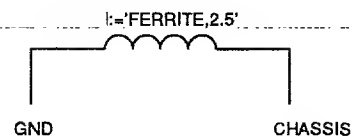
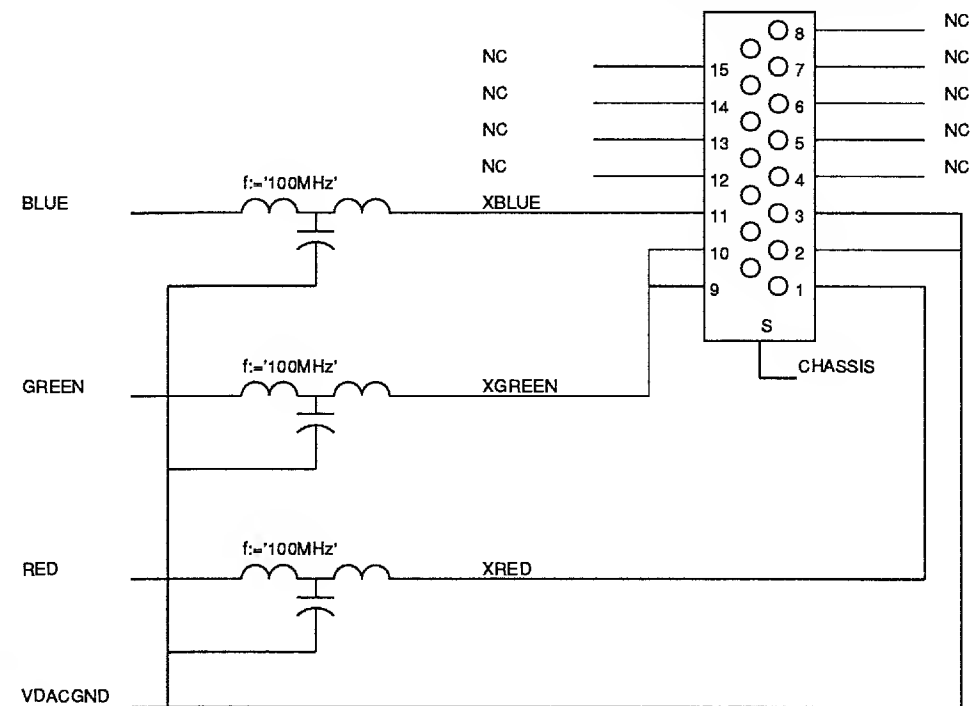
ENG.
M. NIELSEN
BOARD LOCATION:
SHEET 9 OF 21
TOP DOCUMENT NUMBER:

DATE
31 OCT 88
TITLE:
EIA Drivers

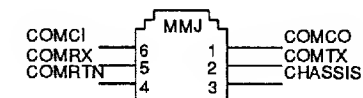
SIZE B CODE CS NUMBER 54-19463-01 REV. CX01



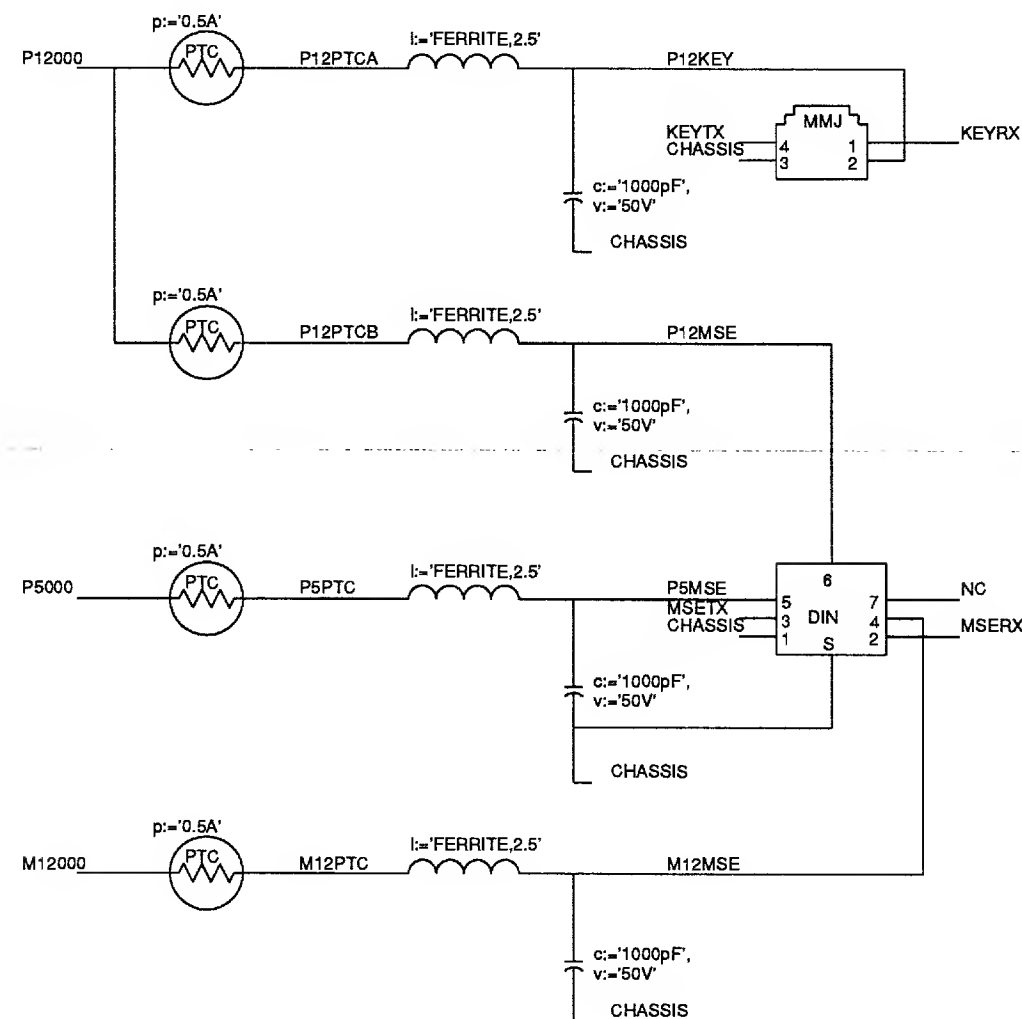
Video Connector



Printer Connector



Comm Connector



Keyboard Connector

Mouse Connector

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USRA:
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DATE
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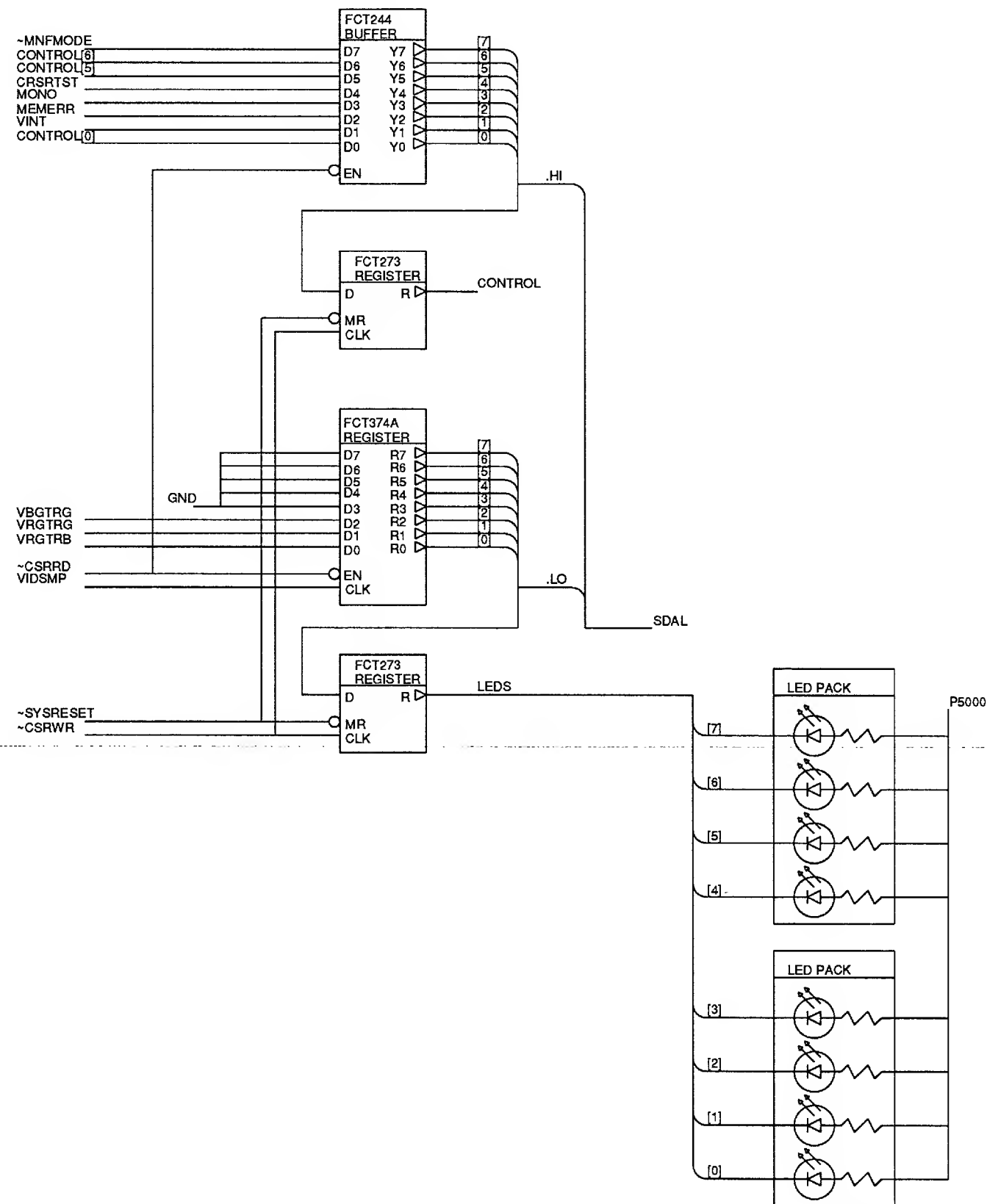
DATE
31 OCT 88

BOARD LOCATION:
SHEET 11 OF 21

TOP DOCUMENT NUMBER:

TITLE:
Serial Connectors

SIZE	CODE	NUMBER	REV.
A	CS	54-19483-01	CX01



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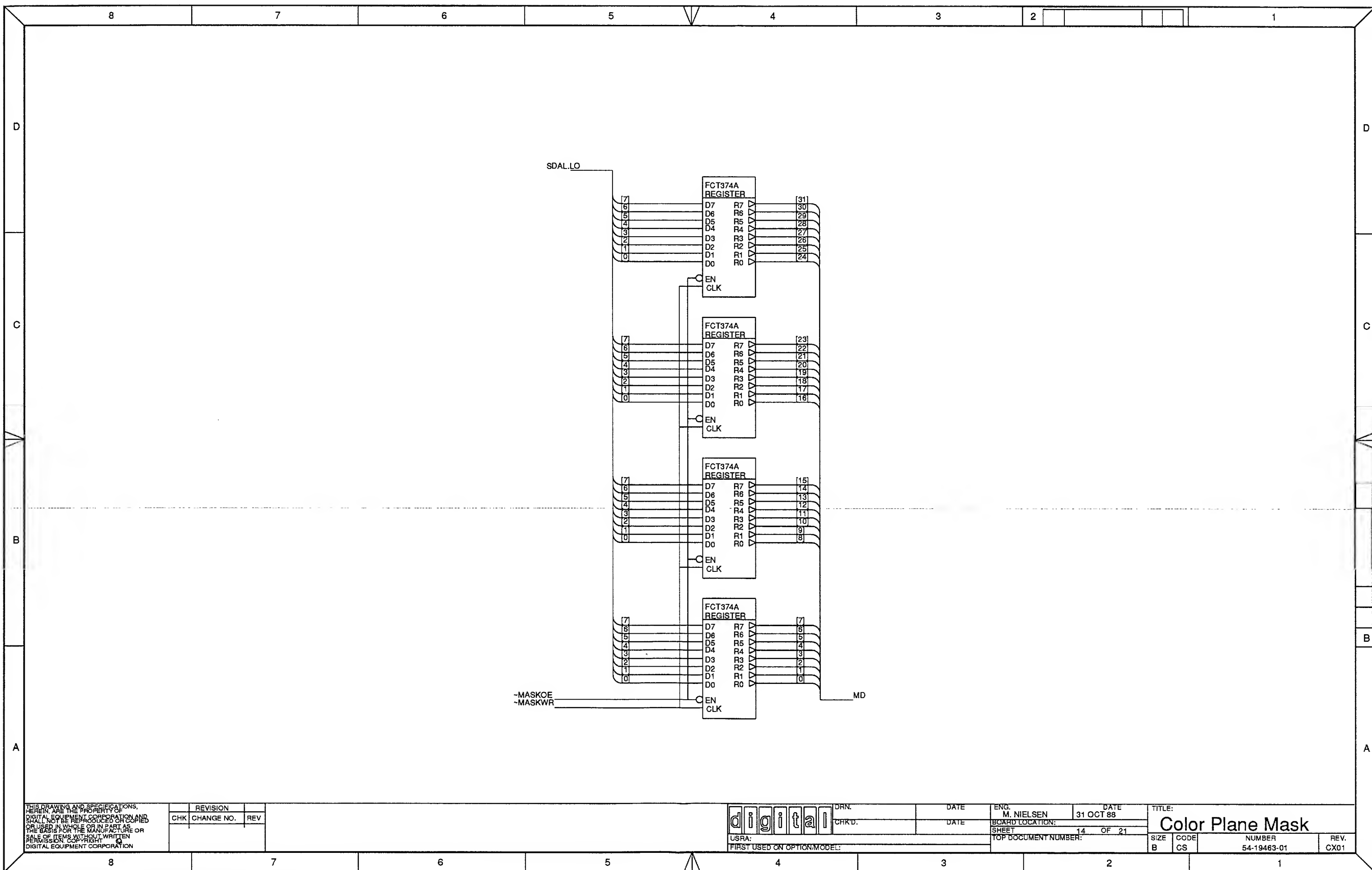
DATE
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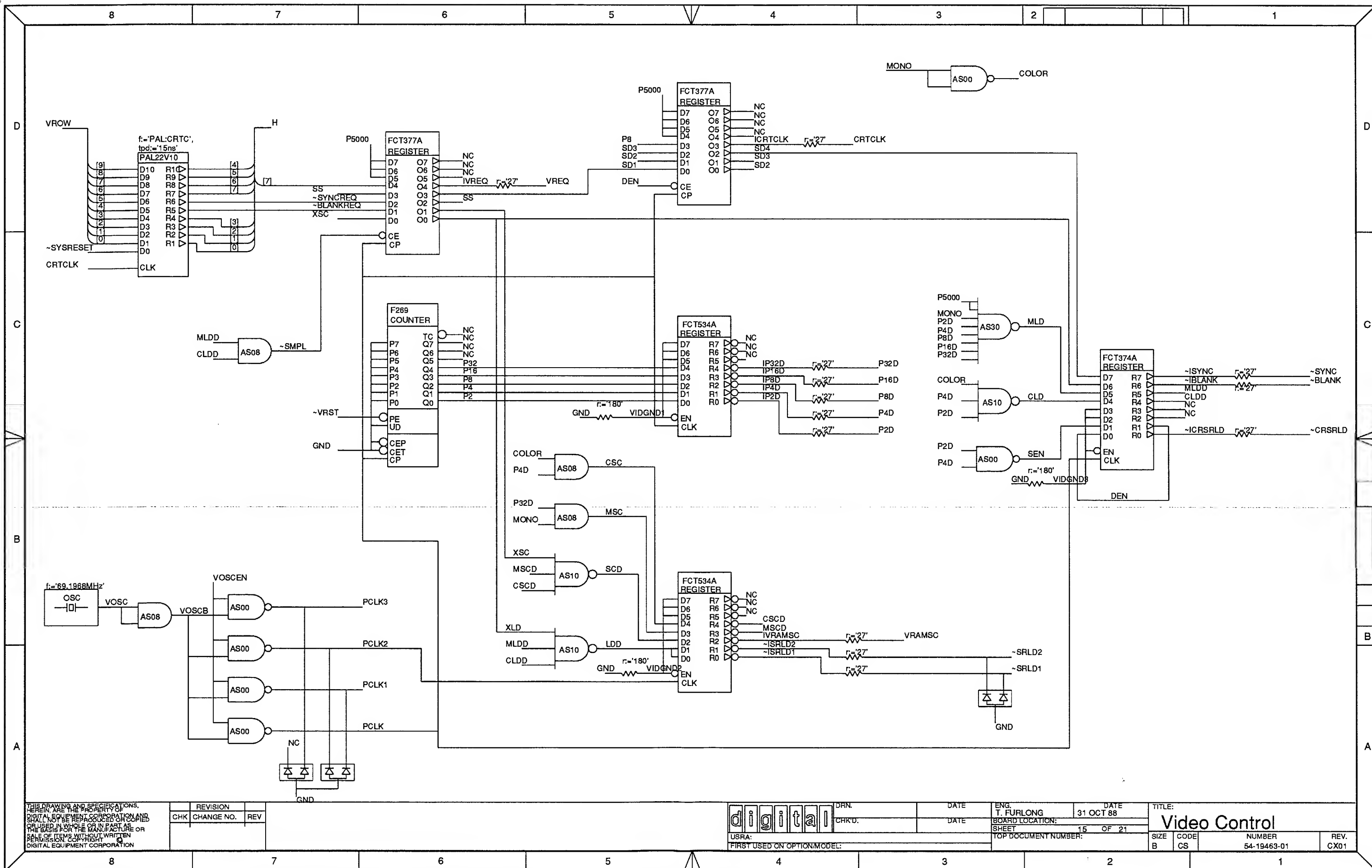
ENG.
M. NIELSEN

DATE
31 OCT 88
SHEET
13 OF 21
TOP DOCUMENT NUMBER:

TITLE:
Control/Status Register

SIZE	CODE	NUMBER	REV.
B	CS	54-19463-01	CX01

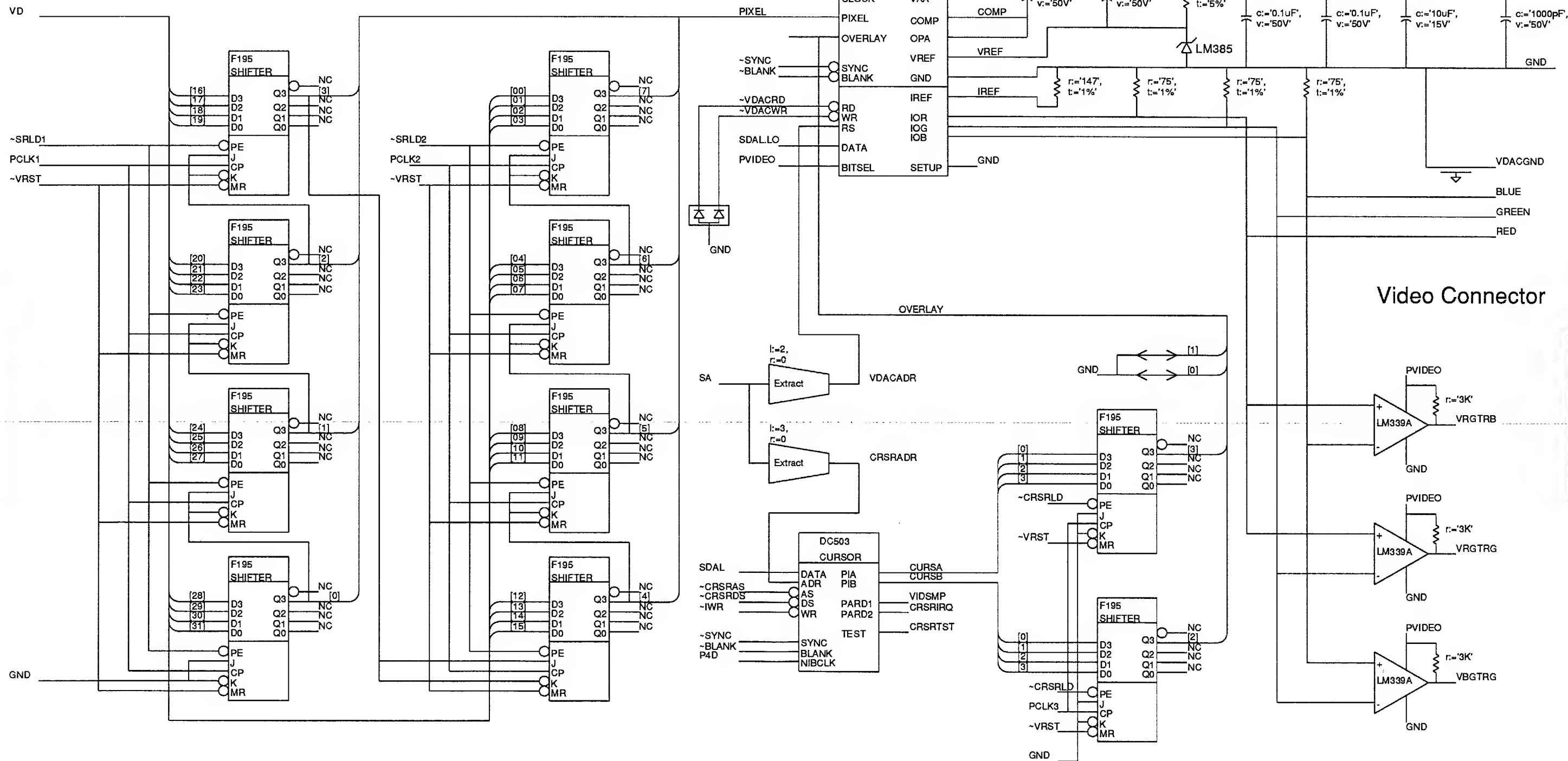




Declarations

```

var
  vdacAdr: bits(3);
  crsrAdr: bits(4);
  
```



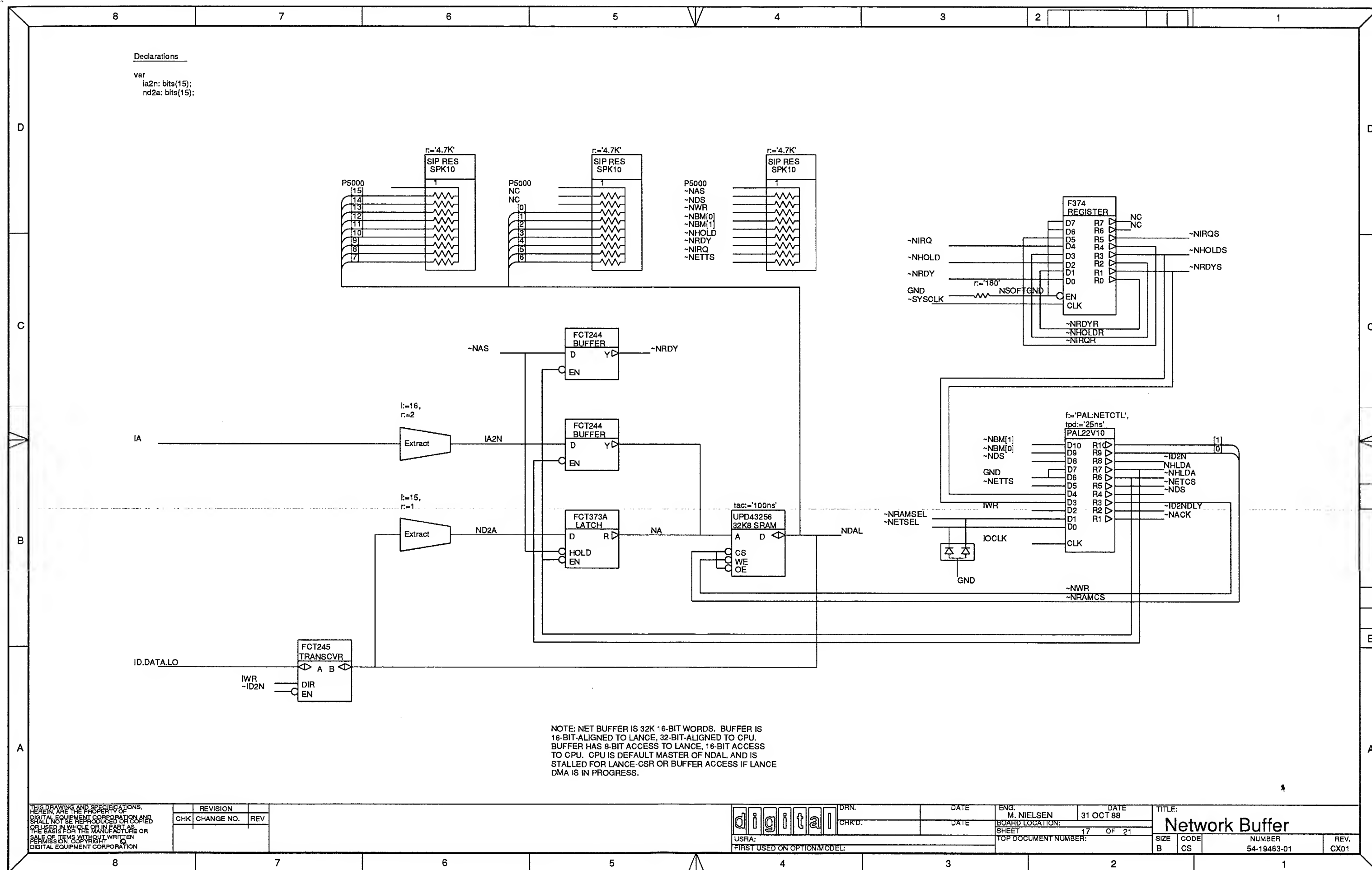
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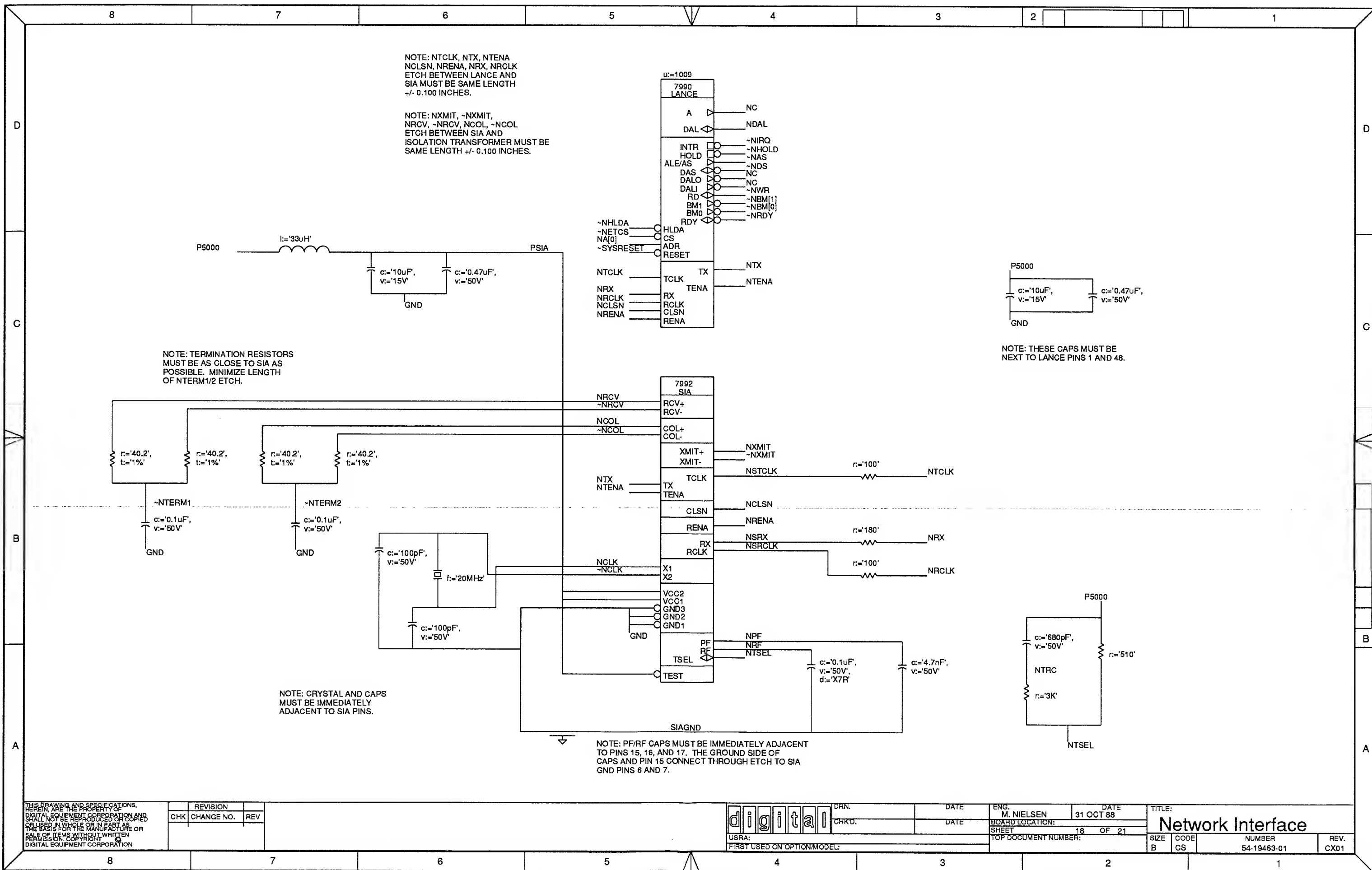
REVISION	CHK	CHANGE NO.	REV

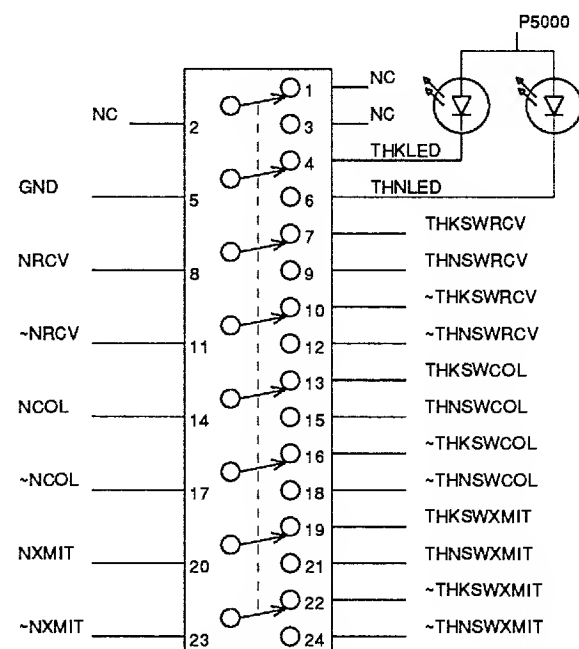
digital DRN.
CHK'D.
USRA:
FIRST USED ON OPTION MODEL:

DATE
DATE
ENG. T. FURLONG
BOARD LOCATION:
SHEET 16 OF 21
TOP DOCUMENT NUMBER:

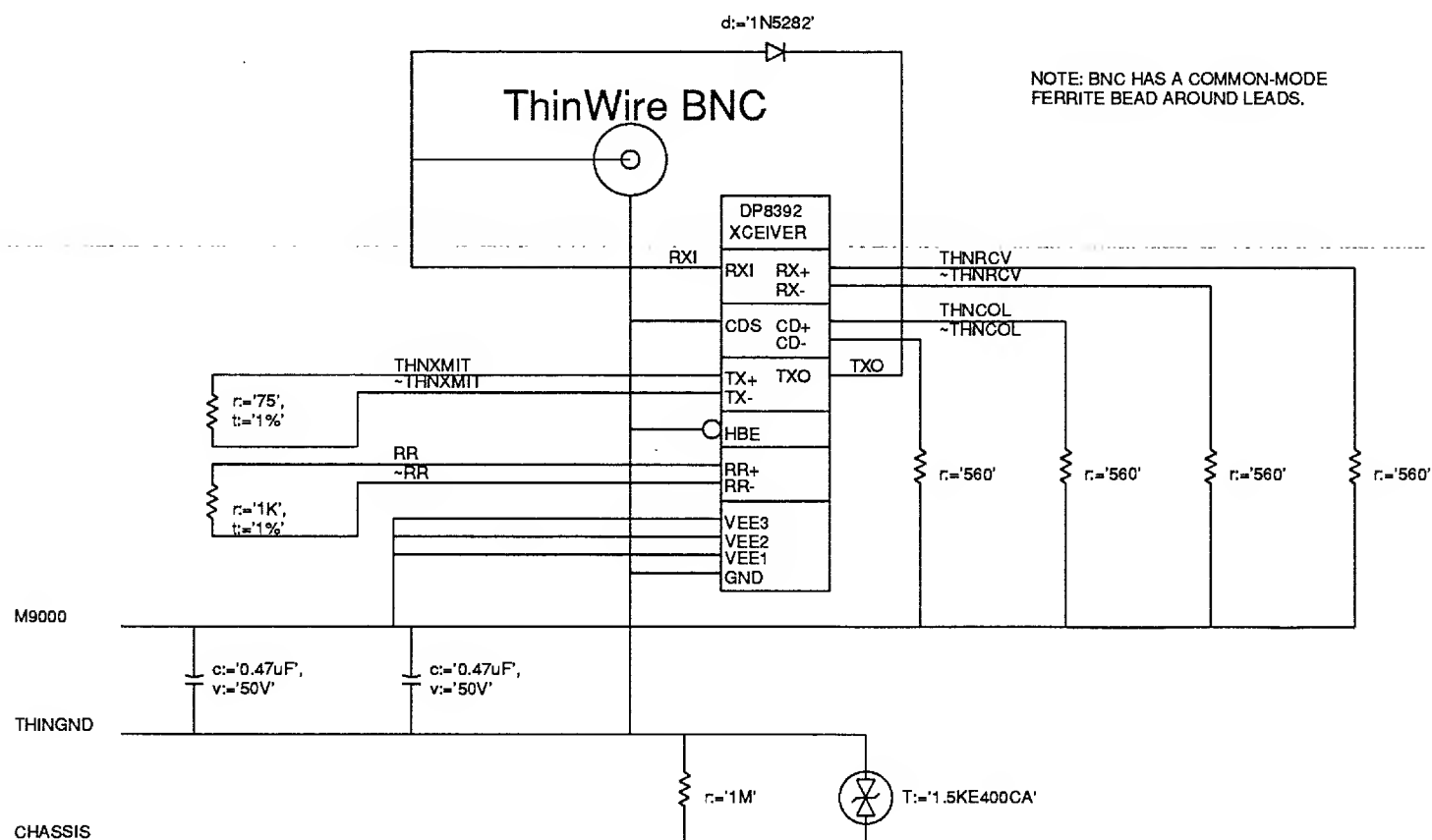
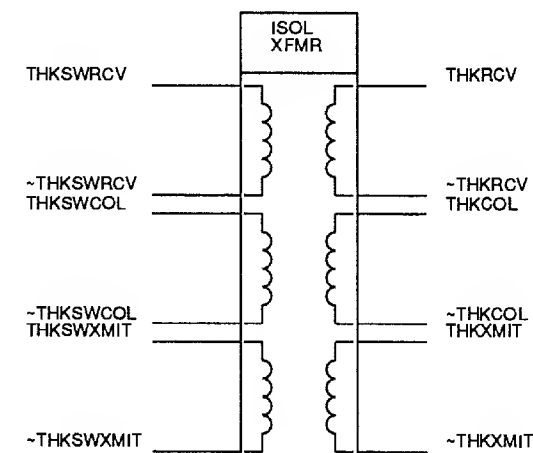
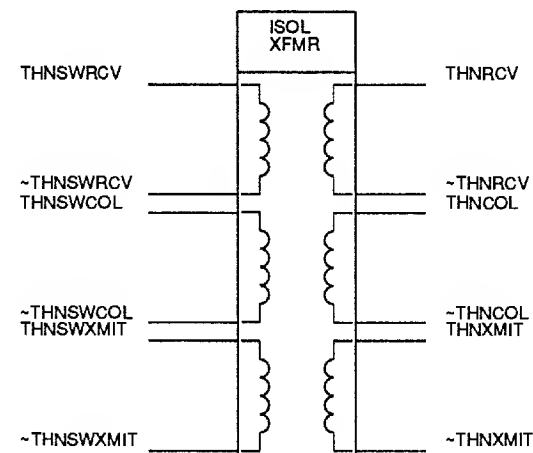
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Video Output
SIZE B CODE CS NUMBER 54-19463-01 REV. CX01



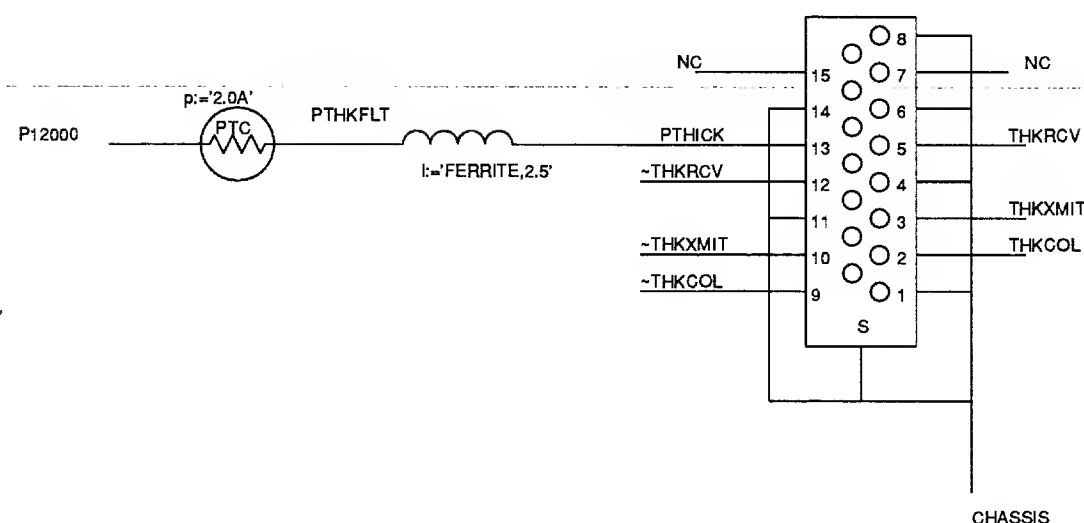




Thin/Thick Selector Switch



ThickWire Connector



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digital
USRA:
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DATE

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DATE

31 OCT 88

TITLE:

Ethernet Transceiver

BOARD LOCATION:

SHEET

19 OF 21

TOP DOCUMENT NUMBER:

SIZE

B

CODE

CS

NUMBER

54-19463-01

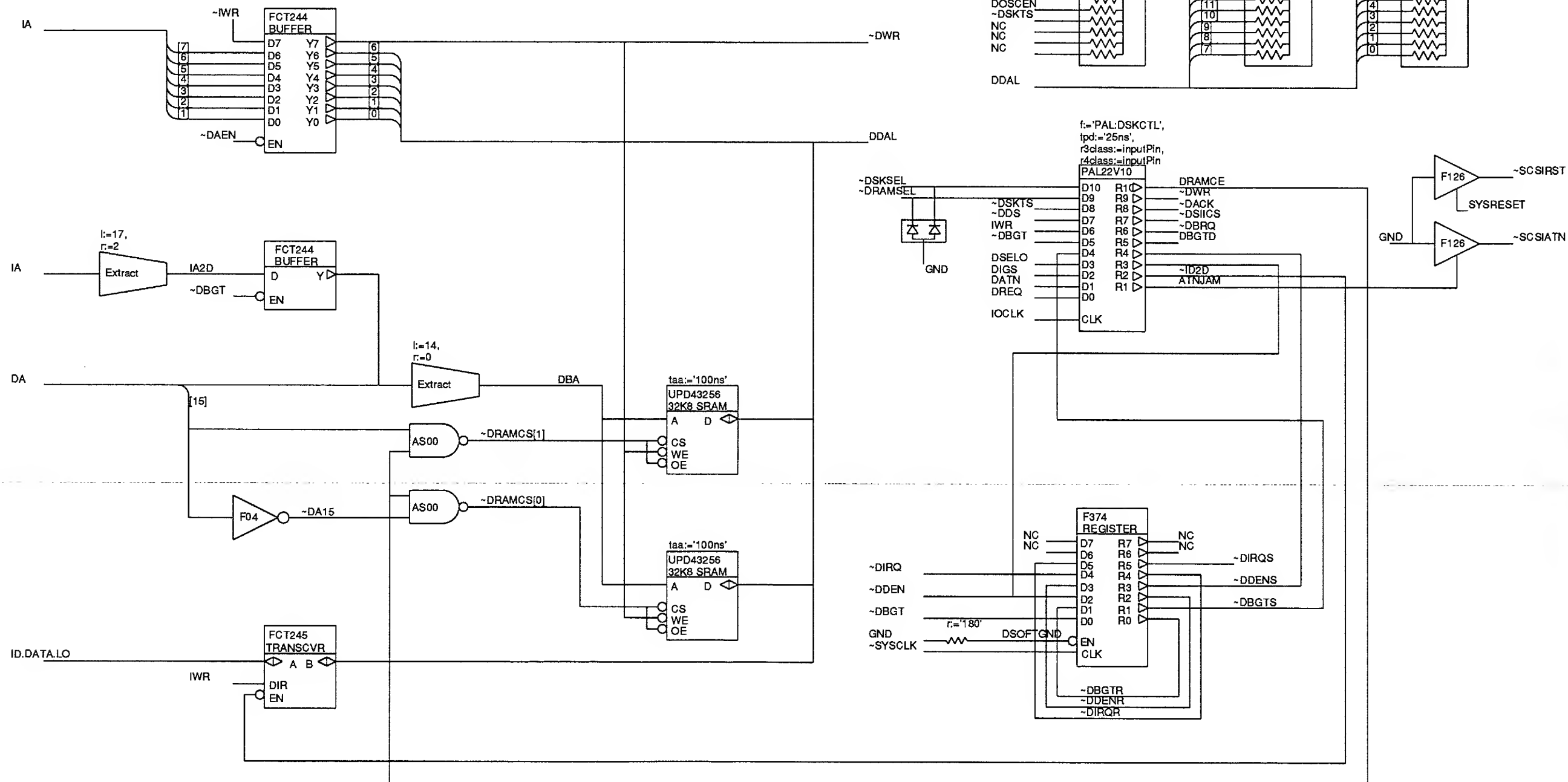
REV.

CX01

Declarations

var
ia2d: bits(16);
dba: bits(15);

NOTE: DISK BUFFER IS 64K 16-BIT WORDS. BUFFER IS 16-BIT-ALIGNED TO SII, 32-BIT-ALIGNED TO CPU. BUFFER HAD 16-BIT ACCESS TO SII AND CPU. SII IS DEFAULT MASTER OF DDAL. CPU IS STALLED FOR SII-CSR OR BUFFER ACCESS IF SII DMA IS IN PROGRESS.



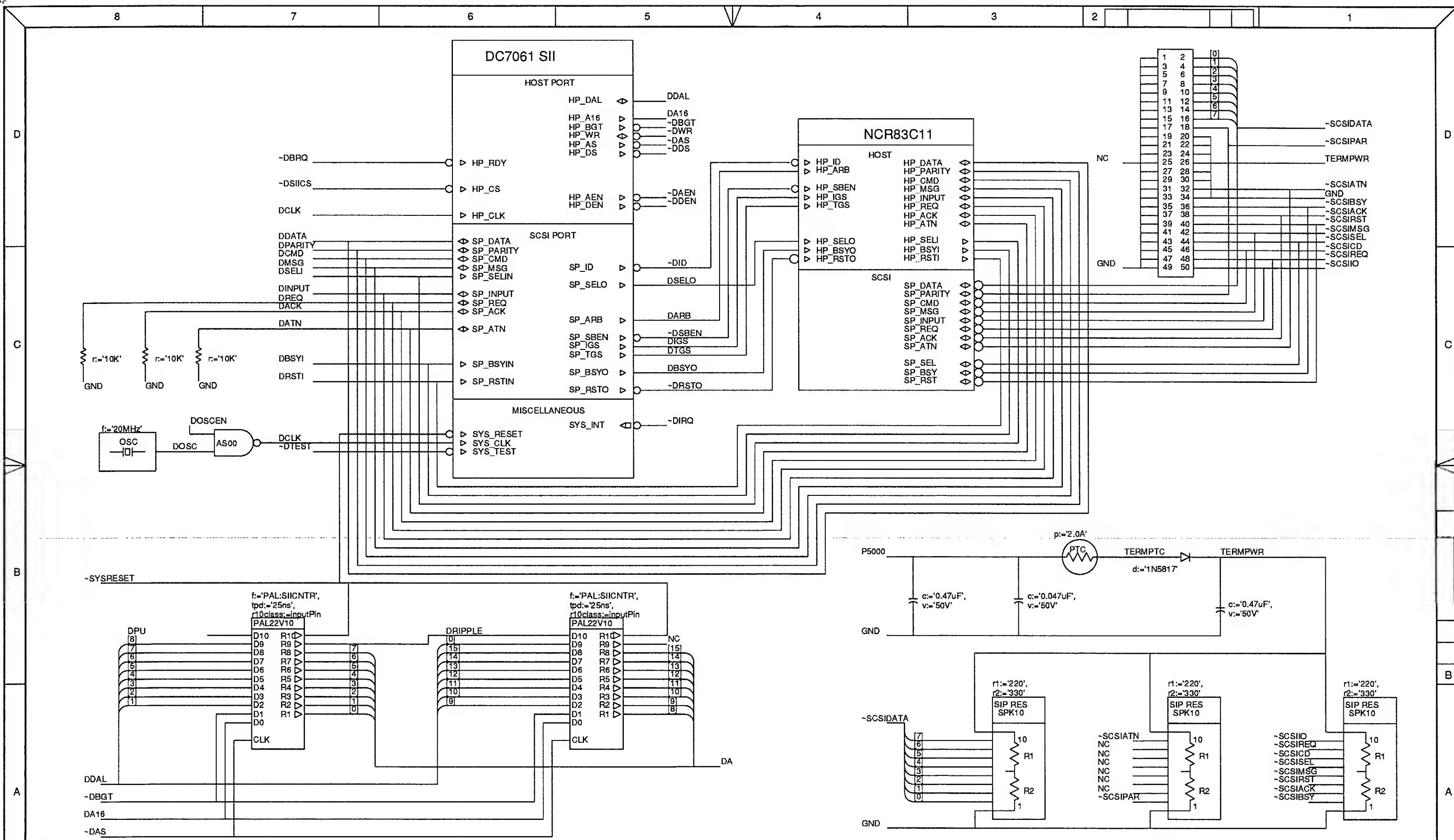
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digital DRN.
CHK'D.
USRA:
FIRST USED ON OPTION MODEL:

DATE	ENG.	DATE	TITLE:
	M. NIELSEN	31 OCT 88	Disk Buffer
	BOARD LOCATION:		
	SHEET	20 OF 21	
	TOP DOCUMENT NUMBER:		

SIZE	CODE	NUMBER	REV.
B	CS	54-19463-01	CX01



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CHK	CHANGE NO.	REV

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USRA:
FIRST USED ON OPTION MODEL:

DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	M. NIELSEN	31 OCT 88	Disk Interface
SHEET		21 OF 21		SIZE
TOP DOCUMENT NUMBER:		NUMBER		CODE
		54-19463-01		REV.
				CX01